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November 1, 2007

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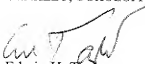
Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 7,085,935
Issued: August 1, 2006
For: MANAGING A SECURE ENVIRONMENT
USING A CHIPSET IN ISOLATED EXECUTION
MODE
Inventor: Ellison, et al.
Our File No. 42390.P8628x

Dear Sir:

Enclosed is the Certificate of Correction for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP



Edwin H. Taylor
Reg. No. 25,129

EHT/sn
Enclosures

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 7,085,935
DATED : August 1, 2006
INVENTOR(S) : Ellison et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 1, at line 22, delete "modem" and insert --modern--.

In column 8, at line 37, delete "handier" and insert --handler--.

In column 8, at line 52, delete "handier" and insert --handler--.

In column 10, at line 48, delete "PE" and insert --processor executive (PE)--.

In column 12, at line 64, delete "PE" and insert --processor executive (PE)--.

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PATENT NO. 7,085,935

Certificate of Correction (PTO Form 1050)-Amended